

Rechargeable Solid State Energy Storage: 12μAh, 3.8V

Features

- All Solid State Construction
- SMT Package and Process
- Lead-Free Reflow Tolerant
- Thousands of Recharge Cycles
- Low Self-Discharge
- Eco-friendly, RoHS compliant



5 mm x 5 mm
DFN SMT Package

Electrical Properties

Output voltage (nominal):	3.8V
Capacity (nominal):	12μAh
Charging source:	4.00V to 4.15V
Recharge time to 80%:	10 minutes
Charge/discharge cycles:	>5000 to 10% DOD

Physical Properties

Package size (DFN):	5 mm x 5 mm x 0.9 mm
Operating temperature:	-40 °C to 70 °C
Storage temperature:	-40 °C to 125 °C

Applications

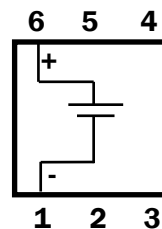
- **Standby supply** for non-volatile SRAM, real-time clocks, controllers, supply supervisors, and other system-critical components.
- **Wireless sensors and RFID tags** and other powered, low duty cycle applications.
- **Localized power source** to keep microcontrollers and other devices alert in standby mode.
- **Power bridging** to provide backup power to system during exchange of primary batteries.
- **Embedded Energy** where bare die can be embedded into modules or co-packaged with other ICs

The EnerChip™ CBC012 is a surface-mount, solid state, thin film, rechargeable energy storage device rated for 12μAh at 3.8V. It is ideal as a localized on-board power source for SRAMs, real-time clocks and microcontrollers which require standby power to retain time or data. It is also suitable for RFID tags, smart sensors, and remote applications which require a miniature, low-cost, and rugged power source. For many applications, the CBC012 is a superior alternative to button cell batteries and super-capacitors.

Because of their solid state design, EnerChip™ storage devices are able to withstand solder reflow temperatures and can be processed in high-volume manufacturing lines similar to conventional semiconductor devices. There are no harmful gases, liquids or special handling procedures, in contrast to traditional rechargeable batteries.

The CBC012 is based on a patented, all solid state, rechargeable energy cell with a nominal 3.8V output. Recharge is fast and simple, with a direct connection to a 4.1V voltage source and no current limiting components. Recharge time is 10 minutes to 80% capacity. Robust design offers thousands of charge/discharge cycles. The CBC012 is packaged in a 5 mm x 5 mm 6-pin DFN package. It is shipped in tubes, tape-and-reel, or waffle pack trays.

Pin Number(s)	Description
1	V-
2,3,4,5	NIC
6	V+
Note: NIC = No Internal Connection	



CBC012 Schematic Representation
Top View

EnerChip™ CBC012 Solid State Energy Storage

Operating Characteristics

Parameter		Condition	Min	Typical	Max	Units
Discharge Cutoff Voltage		25 °C	3.0 ⁽¹⁾	-	-	V
Charge Voltage		25 °C	4.0 ⁽²⁾	4.1	4.3	V
Pulse Discharge Current		25 °C	Variable - see App. Note 1025			-
Cell Resistance (25 °C)		Charge cycle 2	-	2.15	5.35	kΩ
		Charge cycle 1000	-	10.7	21.3	
Self-Discharge (5-yr. average; 25 °C)		Non-recoverable	-	2.5	-	% per year
		Recoverable	-	1.5 ⁽³⁾	-	% per year
Operating Temperature		-	-40	25	+70	°C
Storage Temperature		-	-40	-	+125 ⁽⁴⁾	°C
Recharge Cycles (to 80% of rated capacity; 4.1V charge voltage)	25 °C	10% depth-of-discharge	5000	-	-	cycles
		50% depth-of-discharge	1000	-	-	cycles
	40 °C	10% depth-of-discharge	2500	-	-	cycles
		50% depth-of-discharge	500	-	-	cycles
Recharge Time (to 80% of rated capacity; 4.1V charge voltage) ⁽⁵⁾		Charge cycle 2	-	10	22	minutes
		Charge cycle 1000	-	45	70	
Capacity		50µA discharge; 25 °C	12	-	-	µAh

(1) Failure to cutoff the discharge voltage at 3.0V will result in EnerChip performance degradation.

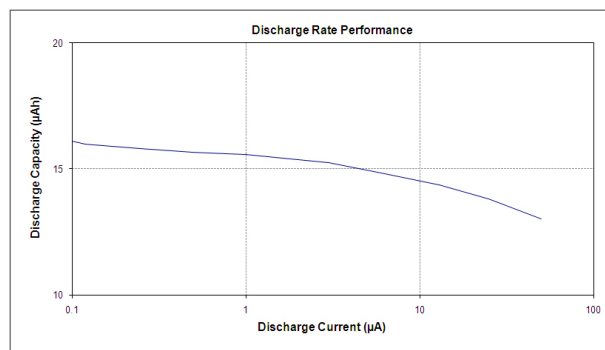
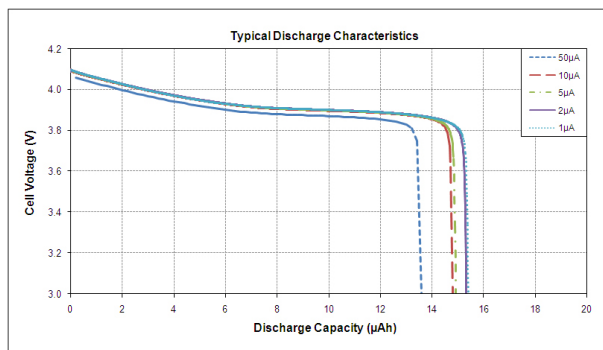
(2) Charging at 4.0V will charge the cell to approximately 70% of its rated capacity.

(3) First month recoverable self-discharge is 4% average.

(4) Storage temperature is for uncharged EnerChip.

(5) EnerChip charging time and cell resistance increase approximately 2x per 10°C decrease in temperature.

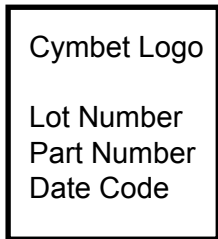
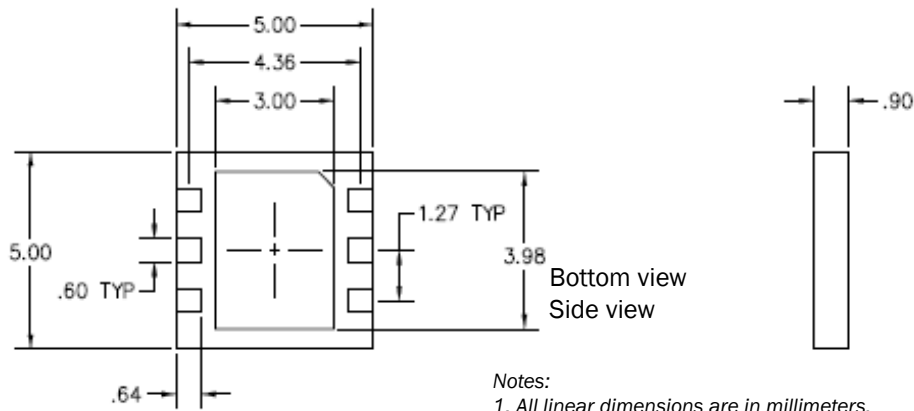
EnerChip Discharge Characteristics



Note: All specifications contained within this document are subject to change without notice

EnerChip™ CBC012 Solid State Energy Storage

Package Dimensions - 6-pin DFN (package code D5)



Chip Labeling
Information
Placement

Pin Number(s)	Description
1	V-
2,3,4,5	NIC
6	V+
Note: NIC = No Internal Connection	

Printed Circuit Board (PCB) Layout Guidelines and Recommendations

Electrical resistance of solder flux residue on PCBs can be low enough to partially or fully discharge the backup energy cell and in some cases can be comparable to the load typically imposed on the cell when delivering power to an integrated circuit in low power mode. Therefore, solder flux must be thoroughly washed from the board following soldering.

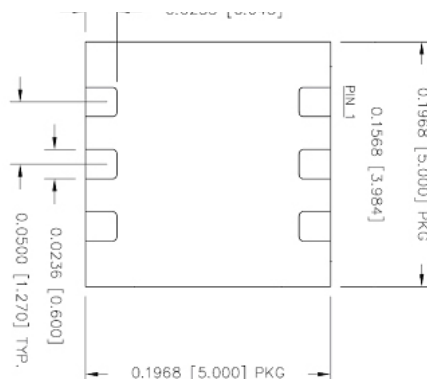
The PCB layout can make this problem worse if the cell's positive and negative terminals are routed near each other and under the package, where it is difficult to wash the flux residue away. In this scenario, solder flux residue can wick from the positive solder pad, covering both the positive pad and the via. This results in a high resistance current path between the EnerChip terminals. Such a current path will make the cell appear to be defective or make the application circuit appear to be drawing too much current. To avoid this situation, make sure positive and negative traces are routed outside of the package footprint, thus ensuring flux residue will not cause a discharge path between the positive and negative pads.

Similarly, a leakage current path can exist from the package lead solder pads to the exposed die pad on the underside of the package as well as any solder pad on the PCB that would be connected to that exposed die pad during the reflow solder process. Therefore, it is strongly recommended that the PCB layout not include a solder pad in the region where the exposed die pad of the package will land. It is sufficient to place PCB solder pads only where the package leads will be. That region of the PCB where the exposed die pad will land must not have any solder pads, traces, or vias.

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When placing a silk screen on the PCB around the perimeter of the package, place the silk screen outside of the package and all metal pads. Failure to observe this precaution can result in package cracking during solder reflow due to the silk screen material interfering with the solder solidification process during cooling.

For the CBC012-D5C the PCB layout of Figure 4 is recommended. Note that there should NOT be a center pad on the PCB that could contact the exposed die pad on the D5C package. Again, this is to reduce the possible number and severity of leakage paths between the EnerChip terminals.



Recommended PCB layout to accommodate CBC012 package.

ADDITIONAL PCB LAYOUT GUIDELINES

There are several PCB layout considerations that must be taken into account when using the EnerChip:

- All capacitors should be placed as close as possible to the EnerChip.
- Power connections should be routed on the layer the EnerChip is placed.
- Any flying capacitor connections must be as short as possible and routed on the same layer as the EnerChip is placed.
- A ground (GND) plane in the PCB should be used for optimal performance of the EnerChip.
- Very low parasitic leakage currents from the VBAT pin to power, signal, and ground connections, can result in unexpected drain of charge from the integrated power source. Maintain sufficient spacing of traces and vias from the VBAT pin and any traces connected to the VBAT pin in order to eliminate parasitic leakage currents that can arise from solder flux or contaminants on the PCB.
- On the EnerChip CC, Pin 1 VBAT and Pin 4 VCHG must be tied together for proper operation.
- See the section on assembly repair techniques for additional information on board layout guidelines.

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HAND SOLDERING TECHNIQUES

When soldering the EnerChip using by hand at a soldering station, adhere to the following guidelines:

- Observe the ESD precautions outlined in this document.
- Never solder an EnerChip that has been partially or fully charged, even if the EnerChip is in a discharged state. This includes wave soldering and reflow soldering.
- Minimize the amount of time that heat is applied to the EnerChip. Using a tweezer-type soldering iron tip that applies heat to two opposite sides or the entire perimeter of the device simultaneously will result in more uniform heating of the package and for a shorter period of time than when soldering one pin or package edge at a time.
- If possible, apply solder paste to the solder pads on the PCB prior to placing the EnerChip on the board; this will promote wetting of the solder and reduce the amount of time the soldering iron is applied to the component and solder pads.
- Place the EnerChip onto the PCB by hand and solder in place rather than grabbing the EnerChip with a heated tweezer-type tip and placing the EnerChip on the board with the iron. This will minimize the amount of time the EnerChip is exposed to heat.
- Most surface mount packages have metal leadframe tie points that do not extend to the bottom surface of the package but are exposed on two more of the package sidewalls. When soldering, ensure that solder does not cover these tie points, as this situation could result in package pins being shorted to one another through the metal leadframe.

ENERCHIP ASSEMBLY REPAIR TECHNIQUES

Should the need arise to replace an EnerChip that has already been soldered to a circuit board, due to battery failure, improper package placement, or other circumstances, it is recommended that the EnerChip being replaced be discarded and replaced with a new EnerChip. When removing the EnerChip from the board, use a tweezer-type soldering iron tip that heats opposite sides of the package simultaneously and lift the package from the board. When applying the new EnerChip to the board, follow the hand soldering guidelines in the previous section.

For QFN-style packages, use a hot air rework station to remove a defective or misplaced EnerChip package. If there are other EnerChips in the vicinity of the EnerChip being replaced, use proper heat shielding to protect the adjacent EnerChip package from the heat source and turn off any heat source that would otherwise be used to heat the bottom of the board during removal of the EnerChip. This will prevent the adjacent EnerChip(s) from being damaged during the rework procedure.

If it is not possible to replace the EnerChip with a new EnerChip, use extreme care when removing the EnerChip from the board to minimize the amount of time heat is applied to the package during removal and re-soldering. Follow the guidelines in the previous section pertaining to hand soldering. Under no circumstances should an EnerChip that has been partially or fully charged - even if subsequently discharged - be subjected to reflow, wave, or hand soldering.

Conductive epoxy may also be used as an attachment method. If the cure temperature is above 70 °C, then a new (i.e., never charged) EnerChip must be used.

EnerChip™ CBC012 Solid State Energy Storage

Ordering Information - available for Last Time Buy until September 12, 2014

EnerChip Part Number	Description	Notes
CBC012-D5C	12μAh EnerChip in 6-pin DFN, tube	Not for new designs
CBC012-D5C-TR1	12μAh EnerChip in 6-pin DFN, Tape/Reel 1k parts	Not for new designs
CBC012-D5C-TR5	12μAh EnerChip in 6-pin DFN, Tape/Reel 5k parts	Not for new designs
CBC012-D5C-WP	12μAh EnerChip in 6-pin DFN, Waffle Pack	Not for new designs

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